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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/034,413	12/27/2001	Christopher Pasqualino	13325US01	1172

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EXAMINER

HARVEY, DAVID E

ART UNIT PAPER NUMBER

2614

DATE MAILED: 09/27/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/034,413	Applicant(s) PASQUALINO	
	Examiner DAVID E HARVEY	Art Unit 2614	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
 - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
 - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
 - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 July 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-23 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-23 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>7/9/2004</u> . | 6) <input type="checkbox"/> Other: _____ |

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1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1, 2, 5, 6, and 8 are rejected under 35 U.S.C. 102(b) as being anticipated by Shyu [US #5,949,255].

As is shown in figure 2, Shyu describes a sync signal processor for processing an incoming periodic sync signal (S1), wherein the processor includes circuitry for detecting the polarity of said sync signal (@ 2, 11-13, 31-33). The polarity detecting circuitry includes:

- 1) A counter (2) for determining a count of the sync signal from one edge to the next;
- 2) Circuitry (e.g. 31, 32, 33) for determining the polarity of the sync signal (@ 35) using said count.

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3. Claims 11 and 12 are rejected under 35 U.S.C. 102(b) as being anticipated by Shyu [US #5,949,255] for the same reason as was set forth for claim 1 above. The following is noted:

As described in Shyu, the sync signal input (S1) comprises either a horizontal sync signal component or a vertical sync signal component of a predetermined, i.e. explicitly set, polarity [e.g. lines 11-30 of column 1].

4. Claim 13 is rejected under 35 U.S.C. 102(b) as being anticipated by Shyu [US #5,949,255] for the same reason as was set forth for claim 1 above.

Horizontal and vertical sync signals are inherently indicative of horizontal and vertical blanking periods, respectively.

5. Claim 7, 8, 10, 11, 14, 22, and 23 are rejected under 35 U.S.C. 102(b) as being anticipated by Shyu [US #5,949,255] for the same reason as was set forth for claim 1 above. The following is noted:

1) It is noted that Shyu describes an alternative embodiment of his invention with respect to figure 6 in which the counter (2) is implemented using an up-down counter wherein the counter is incremented from a first edge to a second and is decremented from the second to a third edge (e.g. 6 of figure 5). The counter causes the

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polarity signal to be set whenever it reaches zero (i.e. @ "turnover");

2) The output of the sync processing circuit represents a "DE signal" in that it enables further processing of the outputted data.

6. Claims 15-21 are rejected under 35 U.S.C. 102(b) as being anticipated by Shyu [US #5,949,255] for the same reason as was set forth for claim 14 above. The following is noted:

1) With respect to claims 15 and 16: In Shyu, the polarity of the sync signal will be equal to the level of the sync pulses prior to the third edge when the counter does not rollover prior to the detection of the third edge (i.e. it will be reset by the next edge before it stops decrementing);

2) With respect to claims 17-19: In Shyu, the polarity of the of the polarity detection signal will be equal to, or the inverse of, the polarity of the sync signal/pulses depending on the setting of the "select" signal;

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3) With respect to claims 20 and 21: As described in Shyu, the sync signal input (S1) comprises either a horizontal sync signal component or a vertical sync signal component of a predetermined, i.e. explicitly set, polarity [e.g. lines 11-30 of column 1].

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7. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

8. Claims 3 and 4 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shyu [US #5,949,255].

I. Shyu disclosed a system as was addressed for claims 11 and 12 above. Namely, as described in Shyu, the sync signal input (Sl) comprises a horizontal sync signal component or a vertical sync signal component [e.g. lines 11-30 of column 1]. In either case, the clock signal (CLK) must be set such that it is substantially faster than the rate of the respective sync signal being received [e.g. lines 31-33 of column 2]. The following is noted:

a) Selecting the clock rate to be the pixel rate when the sync signal input represents the horizontal component would be an obvious choice of design being that the pixel rate is substantially faster than the

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horizontal rate and it typically an integer multiple thereof (i.e. simplifying the counting process to integer multiples).

b) Selecting the clock rate to be the horizontal rate when the sync signal input represents the vertical component would be an obvious choice of design being that the horizontal rate is substantially faster than the horizontal rate and it typically an integer multiple thereof (i.e. simplifying the counting process).

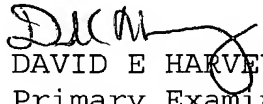
9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to DAVID E HARVEY whose telephone number is (703) 305-4365. The examiner can normally be reached on M-F from 6AM to 3PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, John Miller, can be reached on (703)305-4795. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval

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(PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


DAVID E HARVEY
Primary Examiner
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